(SCHEME OF EXAMINATIONS)

Scheme of Programme Code: 052 Programme Name: MASTER OF TECHNOLOGY (VLSI DESIGN) SchemeID: 310522015001 Sem./Year: 01 SEMESTER
Institution Code: 118 Institution: C-DAC, NOIDA

S.No.	Paper ID	Code	Subject	Credit	Type	Exam	Mode	Kind	Minor	Major	Max. Marks	Pass Marks
01	52601	MEVS601	DIGITAL SYSTEM DESIGN USING VERILOG	4	THEORY	UES	COMPULSORY	MANDATORY	25	75	100	40
02	52603	MEVS603	VLSI TECHNOLOGY	4	THEORY	UES	COMPULSORY	MANDATORY	25	75	100	40
03	52605	MEVS605	ADVANCED VLSI DESIGN	4	THEORY	UES	COMPULSORY	MANDATORY	25	75	100	40
04	48607	MECS607	ADVANCED COMPUTER ARCHITECTURE	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
05	52609	MEVS609	ALGORITHM ANALYSIS AND DESIGN	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
06	48611	MECS611	COMPUTATIONAL TECHNIQUES USING MATLAB	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
07	52611	MEVS611	DSP FOR VLSI DESIGN	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
08	52613	MEVS613	WIRELESS NETWORKS	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
09	52615	MEVS615	RESEARCH METHODOLOGIES	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
10	52651	MEVS651	LAB-1 (DSD LAB)	1	PRACTICAL	UES	COMPULSORY	MANDATORY	40	60	100	40
11	52653	MEVS653	LAB-2 (VLSI TECH. LAB)	1	PRACTICAL	UES	COMPULSORY	MANDATORY	40	60	100	40
12	52655	MEVS655	LAB-3 (AD. VLSI LAB)	1	PRACTICAL	UES	COMPULSORY	MANDATORY	40	60	100	40
13	52657	MEVS657	TERM PAPER-I	2	PRACTICAL	NUES	COMPULSORY	MANDATORY	--	100	100	40

RESULT TABULATION SHEET
Result Prepared on: 27/03/2018
Result Declared on: 27/03/2018
Page No.: 0002

LEGEND

PAPERID(CREDITS)	
Internal	External
TOTAL(GRADE)	

A: Absent C: Cancelled
D: Detained RL: Result Later
CS: Credits Secured
AP: Already Passed

Result of Programme Code: 052 Programme Name: MASTER OF TECHNOLOGY (VLSI DESIGN) Sem./Year: 01 SEMESTER Batch: 2016 Examination: REAPPEAR December, 2017

Institution Code: 118 Institution: C-DAC, NOIDA

S.No.	Photo.	Roll no./Name	52601(4)	52603(4)	52609(4)	CS/Remarks
1		02011805216 PARITOSH CHAMOLA SID: 310000013477 SchemeID: 310522015001	17 36	17 47	16 28	12
			53(B)	64(B+)	44(P)	
2		02411805216 YOGESH SID: 310000013481 SchemeID: 310522015001	48607(4)			0
			15 A			
			A			
3						
4						
5						
6						
7						
8						
9						
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*Passed with Grace Marks
*SID: Student ID; SchemeID: The scheme applicable to the student.
Date on which pdf made: 27/03/2018

RTSID: 2018032705220161180002

(SCHEME OF EXAMINATIONS)

Scheme of Programme Code: 052 Programme Name: MASTER OF TECHNOLOGY (VLSI DESIGN) SchemeID: 310522015001 Sem./Year: 03 SEMESTER
Institution Code: 118 Institution: C-DAC, NOIDA

S.No.	Paper ID	Code	Subject	Credit	Type	Exam	Mode	Kind	Minor	Major	Max. Marks	Pass Marks
01	52701	MEVS701	ALGORITHM FOR VLSI PHYSICAL DESIGN AUTOMATION	4	THEORY	UES	COMPULSORY	MANDATORY	25	75	100	40
02	52703	MEVS703	VLSI DESIGN TEST AND TESTABILITY	4	THEORY	UES	COMPULSORY	MANDATORY	25	75	100	40
03	42705	MEEC705	EMBEDDED SYSTEMS & RTOS	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
04	52705	MEVS705	ANALOG-MIXED SIGNAL DESIGN	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
05	52707	MEVS707	HARDWARE-SOFTWARE CO-DESIGN	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
06	52709	MEVS709	DESIGNING WITH FPGA	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
07	52711	MEVS711	ADVANCED DIGITAL COMMUNICATION SYSTEM	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
08	52713	MEVS713	MEMS AND IC INTEGRATION	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
09	52715	MEVS715	NETWORK ON CHIP DESIGN	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
10	52717	MEVS717	CMOS RF DESIGN	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
11	52719	MEVS719	CIRCUIT INTERCONNECTIONS AND PACKAGING FOR VLSI	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
12	52751	MEVS751	LAB-7 (ALGO. VLSI PDA LAB)	1	PRACTICAL	UES	COMPULSORY	MANDATORY	40	60	100	40
13	52753	MEVS753	LAB-8 (VDTT LAB)	1	PRACTICAL	UES	COMPULSORY	MANDATORY	40	60	100	40
14	52755	MEVS755	TERM PAPER III	2	PRACTICAL	NUES	COMPULSORY	MANDATORY	--	100	100	40
15	52757	MEVS757	MINOR PROJECT	4	PRACTICAL	UES	COMPULSORY	MANDATORY	40	60	100	40

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Result of Programme Code: 052 Programme Name: MASTER OF TECHNOLOGY (VLSI DESIGN) Sem./Year: 03 SEMESTER Batch: 2016 Examination: REGULAR December, 2017

Institution Code: 118 Institution: C-DAC, NOIDA

S.No.	Photo.	Roll no./Name	52701(4)	52703(4)	52705(4)	52709(4)	52713(4)	52751(1)	52753(1)	52755(2)	52757(4)	CS/Remarks
1		00211805216 SUMAN SID: 310000013487 SchemeID: 310522015001	20 46 66(A)	19 40 59(B+)	22 48 70(A)	19 48 67(A)	13 54 67(A)	28 42 70(A)	33 55 88(A+)	– 93 93(O)	33 54 87(A+)	28
2		00311805216 MOHD. ADNAN SID: 310000013488 SchemeID: 310522015001	21 54 75(A+)	20 48 68(A)	19 43 62(B+)	18 53 71(A)	14 49 63(B+)	25 38 63(B+)	32 45 77(A+)	– 90 90(O)	30 36 66(A)	28
3		00411805216 VINEET SID: 310000013489 SchemeID: 310522015001	20 42 62(B+)	22 41 63(B+)	23 40 63(B+)	19 57 76(A+)	20 54 74(A)	31 50 81(A+)	35 50 85(A+)	– 95 95(O)	35 54 89(A+)	28
4		00511805216 SHRAVAN KUMAR SEHGAL SID: 310000013490 SchemeID: 310522015001	21 49 70(A)	24 46 70(A)	22 53 75(A+)	21 53 74(A)	16 57 73(A)	33 40 73(A)	34 50 84(A+)	– 94 94(O)	35 50 85(A+)	28
5		00611805216 MANGAL DEEP GUPTA SID: 310000013468 SchemeID: 310522015001	19 50 69(A)	23 50 73(A)	22 47 69(A)	21 56 77(A+)	20 58 78(A+)	35 52 87(A+)	35 55 90(O)	– 92 92(O)	36 51 87(A+)	28
6		00811805216 SUMANYU PANDE SID: 310000013491 SchemeID: 310522015001	24 60 84(A+)	24 57 81(A+)	23 61 84(A+)	23 59 82(A+)	19 71 90(O)	36 55 91(O)	34 48 82(A+)	– 94 94(O)	35 52 87(A+)	28
7		00911805216 MANJEET KUMAR SID: 310000013469 SchemeID: 310522015001	22 50 72(A)	22 54 76(A+)	21 47 68(A)	23 59 82(A+)	20 52 72(A)	35 51 86(A+)	34 51 85(A+)	– 95 95(O)	35 52 87(A+)	28
8		01411805216 PRASHANT JOSHI SID: 310000013472 SchemeID: 310522015001	11 43 54(B)	21 37 58(B+)	19 45 64(B+)	19 46 65(A)	19 59 78(A+)	25 36 61(B+)	32 50 82(A+)	– 90 90(O)	32 52 84(A+)	28
9		01511805216 ASIYAH ZAINAB NAQVI SID: 310000013473 SchemeID: 310522015001	20 46 66(A)	23 48 71(A)	21 50 71(A)	21 48 69(A)	19 51 70(A)	30 45 75(A+)	32 55 87(A+)	– 95 95(O)	34 54 88(A+)	28
10		01611805216 AAKASH DEEP SID: 310000013474 SchemeID: 310522015001	19 50 69(A)	22 40 62(B+)	20 43 63(B+)	20 58 78(A+)	20 59 79(A+)	33 47 80(A+)	31 53 84(A+)	– 95 95(O)	34 51 85(A+)	28

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Result of Programme Code: 052 Programme Name: MASTER OF TECHNOLOGY (VLSI DESIGN) Sem./Year: 03 SEMESTER Batch: 2016 Examination: REGULAR December, 2017

Institution Code: 118 Institution: C-DAC, NOIDA

S.No.	Photo.	Roll no./Name	52701(4)	52703(4)	52705(4)	52709(4)	52713(4)	52751(1)	52753(1)	52755(2)	52757(4)	CS/Remarks
11		01711805216 TUSHAR CHAUHAN SID: 310000013492 SchemeID: 310522015001	23 45 68(A)	24 47 71(A)	24 45 69(A)	21 52 73(A)	23 62 85(A+)	36 50 86(A+)	34 50 84(A+)	– 91 91(O)	34 48 82(A+)	28
12		01811805216 HARSHITA VARSHNEY SID: 310000013475 SchemeID: 310522015001	20 41 61(B+)	20 43 63(B+)	18 44 62(B+)	17 60 77(A+)	17 48 65(A)	30 43 73(A)	28 40 68(A)	– 79 79(A+)	30 45 75(A+)	28
13		01911805216 AMIT SINGH SID: 310000013476 SchemeID: 310522015001	16 39 55(B+)	18 50 68(A)	17 57 74(A)	15 50 65(A)	10 49 59(B+)	25 33 58(B+)	26 45 71(A)	– 65 65(A)	32 38 70(A)	28
14		02011805216 PARITOSH CHAMOLA SID: 310000013477 SchemeID: 310522015001	17 43 60(B+)	19 37 56(B+)	18 47 65(A)	17 45 62(B+)	12 41 53(B)	27 36 63(B+)	29 50 79(A+)	– 69 69(A)	32 45 77(A+)	28
15		02111805216 ANKUR VATS SID: 310000013478 SchemeID: 310522015001	21 46 67(A)	21 43 64(B+)	21 41 62(B+)	20 58 78(A+)	21 51 72(A)	31 44 75(A+)	32 52 84(A+)	– 91 91(O)	34 46 80(A+)	28
16		02211805216 AMBUJ MAURYA SID: 310000013479 SchemeID: 310522015001	22 56 78(A+)	24 51 75(A+)	19 45 64(B+)	19 54 73(A)	20 55 75(A+)	30 49 79(A+)	32 45 77(A+)	– 93 93(O)	35 47 82(A+)	28
17		02311805216 MANI CHANDRA SHANKAR SID: 310000013480 SchemeID: 310522015001	16 36 52(B)	21 40 61(B+)	17 38 55(B+)	20 48 68(A)	14 44 58(B+)	32 38 70(A)	28 52 80(A+)	– 87 87(A+)	36 53 89(A+)	28
18		02411805216 YOGESH SID: 310000013481 SchemeID: 310522015001	11 33 44(P)	15 37 52(B)	8 35 43(P)	13 42 55(B+)	5 36 41(P)	22 30 52(B)	26 40 66(A)	– 55 55(B+)	25 36 61(B+)	28
19		02511805216 MOHIT CHAHAL SID: 310000013482 SchemeID: 310522015001	12 39 51(B)	19 48 67(A)	18 53 71(A)	19 52 71(A)	11 49 60(B+)	25 35 60(B+)	26 41 67(A)	– 56 56(B+)	28 37 65(A)	28
20		02611805216 DITIPRIYA BANERJEE SID: 310000013483 SchemeID: 310522015001	24 62 86(A+)	22 54 76(A+)	20 50 70(A)	20 63 83(A+)	22 65 87(A+)	34 46 80(A+)	33 55 88(A+)	– 88 88(A+)	34 52 86(A+)	28

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Result of Programme Code: 052 Programme Name: MASTER OF TECHNOLOGY (VLSI DESIGN) Sem./Year: 03 SEMESTER Batch: 2016 Examination: REGULAR December, 2017

S.No.	Photo.	Roll no./Name	Institution Code: 118 Institution: C-DAC, NOIDA																																								CS/Remarks
21		02711805216 AJAY MALL SID: 310000013484 SchemeID: 310522015001	52701(4)		52703(4)		52705(4)		52709(4)		52713(4)		52751(1)		52753(1)		52755(2)		52757(4)																								28
			15	47	19	48	18	40	17	52	14	56	31	42	31	51	–	87	34	46																						
			62(B+)		67(A)		58(B+)		69(A)		70(A)		73(A)		82(A+)		87(A+)		80(A+)																						
22		02811805216 DISHA TANWAR SID: 310000013485 SchemeID: 310522015001	52701(4)		52703(4)		52705(4)		52709(4)		52713(4)		52751(1)		52753(1)		52755(2)		52757(4)																								28
			16	41	22	46	16	37	19	50	16	44	32	45	33	50	–	91	33	51																						
			57(B+)		68(A)		53(B)		69(A)		60(B+)		77(A+)		83(A+)		91(O)		84(A+)																						
23		02911805216 VINAY MISHRA SID: 310000013486 SchemeID: 310522015001	52701(4)		52703(4)		52705(4)		52709(4)		52713(4)		52751(1)		52753(1)		52755(2)		52757(4)																								28
			18	43	20	37	15	39	21	41	15	36	34	48	32	48	–	86	34	47																						
			61(B+)		57(B+)		54(B)		62(B+)		51(B)		82(A+)		80(A+)		86(A+)		81(A+)																						
24																																											
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(SCHEME OF EXAMINATIONS)

Scheme of Programme Code: 052 Programme Name: MASTER OF TECHNOLOGY (VLSI DESIGN) SchemeID: 310522015001 Sem./Year: 01 SEMESTER
Institution Code: 118 Institution: C-DAC, NOIDA

S.No.	Paper ID	Code	Subject	Credit	Type	Exam	Mode	Kind	Minor	Major	Max. Marks	Pass Marks
01	52601	MEVS601	DIGITAL SYSTEM DESIGN USING VERILOG	4	THEORY	UES	COMPULSORY	MANDATORY	25	75	100	40
02	52603	MEVS603	VLSI TECHNOLOGY	4	THEORY	UES	COMPULSORY	MANDATORY	25	75	100	40
03	52605	MEVS605	ADVANCED VLSI DESIGN	4	THEORY	UES	COMPULSORY	MANDATORY	25	75	100	40
04	48607	MECS607	ADVANCED COMPUTER ARCHITECTURE	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
05	52609	MEVS609	ALGORITHM ANALYSIS AND DESIGN	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
06	48611	MECS611	COMPUTATIONAL TECHNIQUES USING MATLAB	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
07	52611	MEVS611	DSP FOR VLSI DESIGN	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
08	52613	MEVS613	WIRELESS NETWORKS	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
09	52615	MEVS615	RESEARCH METHODOLOGIES	4	THEORY	UES	ELECTIVE	DROPPABLE	25	75	100	40
10	52651	MEVS651	LAB-1 (DSD LAB)	1	PRACTICAL	UES	COMPULSORY	MANDATORY	40	60	100	40
11	52653	MEVS653	LAB-2 (VLSI TECH. LAB)	1	PRACTICAL	UES	COMPULSORY	MANDATORY	40	60	100	40
12	52655	MEVS655	LAB-3 (AD. VLSI LAB)	1	PRACTICAL	UES	COMPULSORY	MANDATORY	40	60	100	40
13	52657	MEVS657	TERM PAPER-I	2	PRACTICAL	NUES	COMPULSORY	MANDATORY	--	100	100	40

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Result of Programme Code: 052 Programme Name: MASTER OF TECHNOLOGY (VLSI DESIGN) Sem./Year: 01 SEMESTER Batch: 2017 Examination: REGULAR December, 2017

Institution Code: 118 Institution: C-DAC, NOIDA

S.No.	Photo.	Roll no./Name	52601(4)	52603(4)	52605(4)	48607(4)	52609(4)	52651(1)	52653(1)	52655(1)	52657(2)	CS/Remarks
1		00311805217 PRAGYA TRIPATHI SID: 310000013760 SchemeID: 310522015001	18 58	23 56	20 60	21 47	23 48	37 53	38 54	34 52	– 83	25
			76(A+)	79(A+)	80(A+)	68(A)	71(A)	90(O)	92(O)	86(A+)	83(A+)	
2		00411805217 NEHA GUPTA SID: 310000013761 SchemeID: 310522015001	21 55	16 50	17 51	22 51	22 31	36 54	32 53	32 46	– 74	25
			76(A+)	66(A)	68(A)	73(A)	53(B)	90(O)	85(A+)	78(A+)	74(A)	
3		00511805217 ROHAN ROY SID: 310000013762 SchemeID: 310522015001	21 55	19 55	18 45	19 52	23 40	38 56	36 55	36 47	– 85	25
			76(A+)	74(A)	63(B+)	71(A)	63(B+)	94(O)	91(O)	83(A+)	85(A+)	
4		00611805217 MUDIT SINGH SID: 310000013763 SchemeID: 310522015001	22 57	18 47	15 30	18 44	15 27	29 50	26 49	29 42	– 72	25
			79(A+)	65(A)	45(C)	62(B+)	42(P)	79(A+)	75(A+)	71(A)	72(A)	
5		00711805217 SAPNA TAYAL SID: 310000013764 SchemeID: 310522015001	25 66	24 61	24 63	21 60	24 43	38 53	32 54	36 45	– 88	25
			91(O)	85(A+)	87(A+)	81(A+)	67(A)	91(O)	86(A+)	81(A+)	88(A+)	
6		00811805217 NAVAL KISHOR TEWARI SID: 310000013765 SchemeID: 310522015001	9 30	6 31	4 17	10 37	6 25	18 30	23 32	22 35	– 65	17
			40(P)*	40(P)*	21(F)	47(C)	31(F)	48(C)	55(B+)	57(B+)	65(A)	
7		00911805217 AMAN KUMAR SID: 310000013766 SchemeID: 310522015001	25 54	16 47	18 51	21 52	21 31	38 53	35 54	35 48	– 80	25
			79(A+)	63(B+)	69(A)	73(A)	52(B)	91(O)	89(A+)	83(A+)	80(A+)	
8		01011805217 SIMRANJEET SINGH SID: 310000013767 SchemeID: 310522015001	19 50	19 39	18 53	18 49	19 34	29 48	32 48	32 42	– 74	25
			69(A)	58(B+)	71(A)	67(A)	53(B)	77(A+)	80(A+)	74(A)	74(A)	
9		01111805217 KANIKA JUNEJA SID: 310000013768 SchemeID: 310522015001	24 56	17 42	14 54	19 49	23 33	34 56	35 55	35 45	– 83	25
			80(A+)	59(B+)	68(A)	68(A)	56(B+)	90(O)	90(O)	80(A+)	83(A+)	
10		01211805217 ANHAD SINGH PURBA SID: 310000013769 SchemeID: 310522015001	18 41	12 41	11 43	15 42	23 41	34 59	29 54	35 46	– 88	25
			59(B+)	53(B)	54(B)	57(B+)	64(B+)	93(O)	83(A+)	81(A+)	88(A+)	

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Result of Programme Code: 052 Programme Name: MASTER OF TECHNOLOGY (VLSI DESIGN) Sem./Year: 01 SEMESTER Batch: 2017 Examination: REGULAR December, 2017

Institution Code: 118 Institution: C-DAC, NOIDA

S.No.	Photo.	Roll no./Name	52601(4)	52603(4)	52605(4)	48607(4)	52609(4)	52651(1)	52653(1)	52655(1)	52657(2)	CS/Remarks
11		01311805217 TARA KUMAR MISHRA SID: 310000013770 SchemeID: 310522015001	15 50 65(A)	10 50 60(B+)	11 37 48(C)	19 47 66(A)	20 28 48(C)	28 44 72(A)	26 32 58(B+)	28 36 64(B+)	– 86 86(A+)	25
12		01411805217 MEGHA AGGARWAL SID: 310000013771 SchemeID: 310522015001	15 49 64(B+)	10 54 64(B+)	12 39 51(B)	15 51 66(A)	10 33 43(P)	31 42 73(A)	25 50 75(A+)	27 46 73(A)	– 72 72(A)	25
13		01611805217 KUMARI SHWETA SID: 310000013772 SchemeID: 310522015001	19 50 69(A)	14 54 68(A)	14 45 59(B+)	15 47 62(B+)	14 35 49(C)	30 42 72(A)	30 35 65(A)	30 48 78(A+)	– 85 85(A+)	25
14		01711805217 JIJIVISHA SINGH SID: 310000013773 SchemeID: 310522015001	15 32 47(C)	14 59 73(A)	15 44 59(B+)	15 50 65(A)	18 37 55(B+)	30 35 65(A)	30 35 65(A)	27 48 75(A+)	– 78 78(A+)	25
15		01811805217 STUTI AGGARWAL SID: 310000013774 SchemeID: 310522015001	21 55 76(A+)	21 58 79(A+)	22 55 77(A+)	19 55 74(A)	22 52 74(A)	30 50 80(A+)	36 52 88(A+)	30 50 80(A+)	– 88 88(A+)	25
16		01911805217 BHAVY KUMAR SID: 310000013775 SchemeID: 310522015001	17 41 58(B+)	17 55 72(A)	12 43 55(B+)	18 50 68(A)	21 31 52(B)	29 42 71(A)	35 36 71(A)	30 42 72(A)	– 74 74(A)	25
17		02011805217 JYOTI KAMAL JHA SID: 310000013776 SchemeID: 310522015001	15 38 53(B)	11 55 66(A)	10 40 50(B)	10 54 64(B+)	12 43 55(B+)	28 35 63(B+)	29 51 80(A+)	23 38 61(B+)	– 75 75(A+)	25
18		02111805217 NUPUR SID: 310000013777 SchemeID: 310522015001	15 36 51(B)	10 40 50(B)	12 31 43(P)	18 47 65(A)	18 28 46(C)	28 53 81(A+)	27 35 62(B+)	31 42 73(A)	– 88 88(A+)	25
19		02211805217 AWANTIKA SID: 310000013778 SchemeID: 310522015001	16 42 58(B+)	15 51 66(A)	20 48 68(A)	16 49 65(A)	18 43 61(B+)	30 55 85(A+)	30 52 82(A+)	31 42 73(A)	– 85 85(A+)	25
20		02311805217 RITIKA SINGH SID: 310000013779 SchemeID: 310522015001	17 50 67(A)	14 56 70(A)	12 39 51(B)	16 57 73(A)	17 36 53(B)	31 52 83(A+)	33 35 68(A)	29 48 77(A+)	– 80 80(A+)	25

*Passed with Grace Marks
*SID: Student ID; SchemeID: The scheme applicable to the student.
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Page No.: 0010

LEGEND

PAPERID (CREDITS)	
Internal	External
TOTAL (GRADE)	

A: Absent C: Cancelled
D: Detained RL: Result Later
CS: Credits Secured
AP: Already Passed

Result of Programme Code: 052 Programme Name: MASTER OF TECHNOLOGY (VLSI DESIGN) Sem./Year: 01 SEMESTER Batch: 2017 Examination: REGULAR December, 2017

Institution Code: 118 Institution: C-DAC, NOIDA

S.No.	Photo.	Roll no./Name	52601(4)	52603(4)	52605(4)	48607(4)	52609(4)	52651(1)	52653(1)	52655(1)	52657(2)	CS/Remarks
21		02411805217 KRITI SID: 310000013780 SchemeID: 310522015001	23 42	21 62	18 54	22 52	21 46	37 53	37 55	35 51	– 88	25
			65(A)	83(A+)	72(A)	74(A)	67(A)	90(O)	92(O)	86(A+)	88(A+)	
22		02511805217 HITESH KUMAR SID: 310000013781 SchemeID: 310522015001	14 30	12 44	13 38	15 47	18 27	28 43	31 33	30 42	– 79	25
			44(P)	56(B+)	51(B)	62(B+)	45(C)	71(A)	64(B+)	72(A)	79(A+)	
23		50111805217 SURBHI GANGWAR SID: 310000013936 SchemeID: 310522015001	17 34	13 48	16 41	14 41	10 28	29 42	25 34	28 42	– 73	25
			51(B)	61(B+)	57(B+)	55(B+)	40(P)*	71(A)	59(B+)	70(A)	73(A)	
24		50211805217 PANKAJ KUMAR SID: 310000013937 SchemeID: 310522015001	14 31	14 41	14 42	14 48	21 29	27 40	30 36	30 43	– 79	25
			45(C)	55(B+)	56(B+)	62(B+)	50(B)	67(A)	66(A)	73(A)	79(A+)	
25												
26												
27												
28												
29												
30												

*Passed with Grace Marks
*SID: Student ID; SchemeID: The scheme applicable to the student.
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